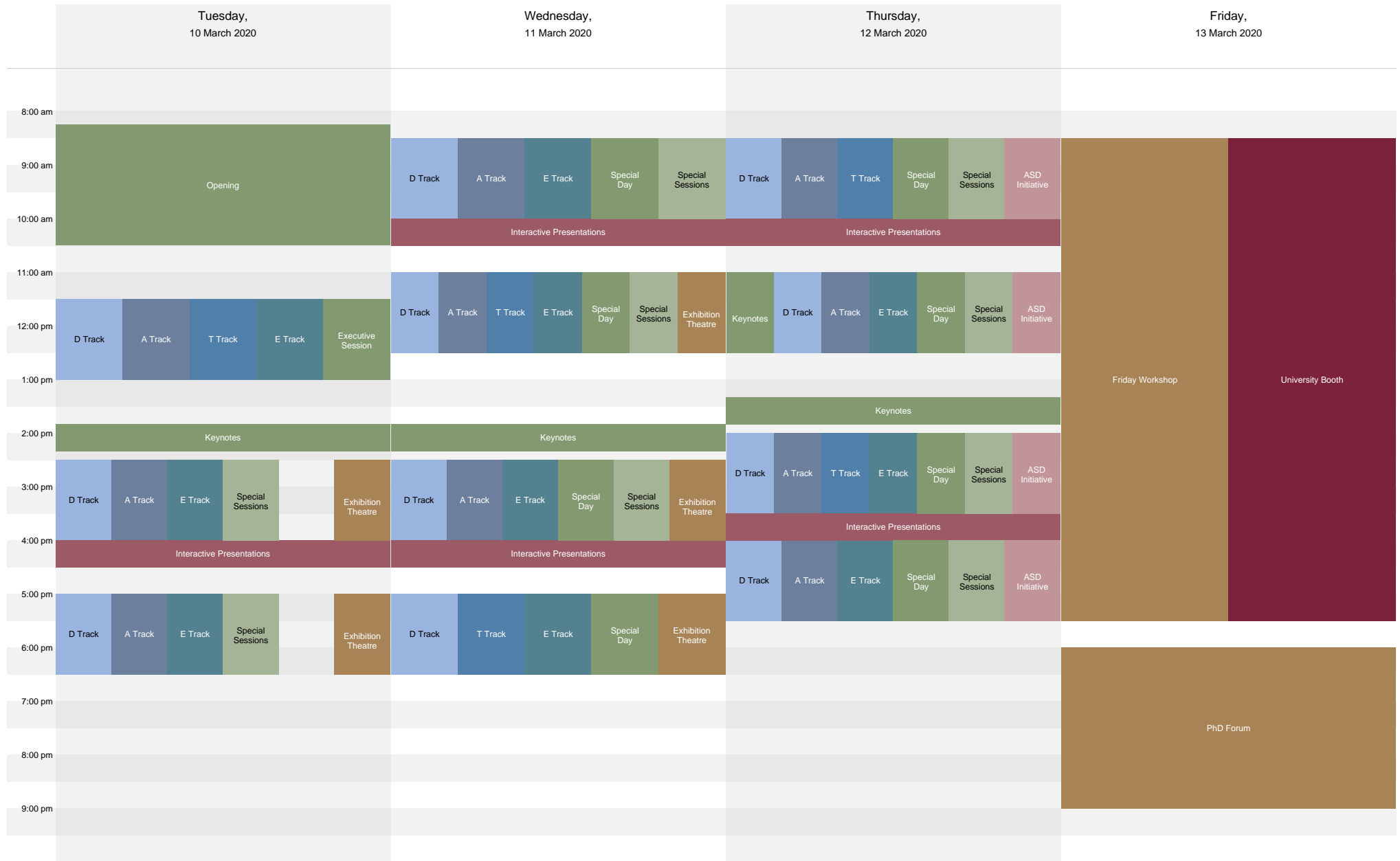


Week overview



Overview: Tuesday, 10 Mar, 2020

	Amphithéâtre Dauphine	Amphithéâtre Jean Prouve	Chamrousse	Autrans	Stendhal	Bayard	Lesdiguières	Berlioz	Exhibition Theatre	Poster Area
8:00 am										
9:00 am	1.1 – Opening Session: Plenary, Awards Ceremony & Keynote Addresses									
10:00 am										
11:00 am										
12:00 pm		2.1 – Memories for Emerging Applications	2.2 – Hardware-assisted Secure Systems	2.3 – Fueling the future of computing: 3D, TFT, or disruptive memories?	2.4 – Challenges in Analog Design Automation & Security	2.5 – Pruning Techniques for Embedded Neural Networks	2.6 – Improving reliability and fault tolerance of advanced memories	2.7 – Optimizing emerging applications for power-efficient computing		
1:00 pm										
2:00 pm		3.0 – Lunchtime Keynote Session								
3:00 pm		3.1 – Special Session: Architectures for Emerging Technologies	3.2 – Accelerating Design Space Exploration	3.3 – EU/ESA projects on Heterogeneous Computing	3.4 – Accelerating Neural Networks and Vision Workloads	3.5 – Parallel real-time systems	3.6 – NoC in the age of neural network and approximate computing	3.7 – Augmented and Assisted Living: A reality	3.8 – Solutions for AI on Chip using Neuromorphic Hardware, for AI from Edge to Cloud and for Power-Efficiency	
4:00 pm										IP1 Interactive Presentations
5:00 pm		4.1 – Hardware-enabled security	4.2 – Timing in System-Level Modeling and Simulation	4.3 – EU Projects on Nanoelectronics with CMOS and alternative technologies	4.4 – Some run it hot, others do not	4.5 – Adaptation and optimization for real-time systems	4.6 – Artificial Intelligence and Secure Systems	4.7 – Future computing fabrics: security and design integration	4.8 – Solutions for SiP Implementation, In-System Test and NoC/SoC Test	
6:00 pm										
7:00 pm										

Overview: Wednesday, 11 Mar, 2020

	Amphithéâtre Jean Prouve	Chamrousse	Autrans	Stendhal	Bayard	Lesdiguières	Berlioz	Exhibition Theatre	Poster Area
8:00 am									
9:00 am	5.1 – Special Day on "Embedded AI": Tutorial Overviews	5.2 – Machine Learning Approaches to Analog Design	5.3 – Special Session: Secure Composition of Hardware Systems	5.4 – New Frontiers in Formal Verification for Hardware	5.5 – Model-Based Analysis and Security	5.6 – Logic synthesis towards fast, compact, and secure designs	5.7 – Stochastic Computing	5.8 – Special Session: HLS for AI HW	
10:00 am									IP2 Interactive Presentations
11:00 am	6.1 – Special Day on "Embedded AI": Emerging Devices, Circuits and Systems	6.2 – Secure and fast memory and storage	6.3 – Special Session: Modern Logic Reasoning Methods for Functional ECO	6.4 – Microarchitecture to the rescue of memory	6.5 – Efficient Data Representations in Neural Networks	6.6 – From DFT to Yield Optimization	6.7 – Safety and efficiency for smart automotive and energy systems	6.8 – Solutions for EDA Design Environments	
12:00 pm									
1:00 pm									
2:00 pm	7.0 – LUNCHTIME KEYNOTE SESSION								
3:00 pm	7.1 – Special Day on "Embedded AI": Industry AI chips	7.2 – Reconfigurable Systems and Architectures	7.3 – Special Session: Realizing Quantum Algorithms on Real Quantum Computing Devices	7.4 – Simulation and verification: where real issues meet scientific innovation	7.5 – Runtime support for multi/many cores	7.6 – Attacks on Hardware Architectures	7.7 – Self-Adaptive and Learning Systems	7.8 – SystemC-based virtual prototyping: from SoC modeling to the digital twin revolution	
4:00 pm									IP3 Interactive Presentations
5:00 pm	8.1 – Special Day on "Embedded AI": Neuromorphic chips and systems	8.2 – We are all hackers: design and detection of security attacks	8.3 – Optimizing System-Level Design for Machine Learning	8.4 – Architectural and Circuit Techniques toward Energy-efficient Computing	8.5 – CNN Dataflow Optimizations	8.6 – Microarchitecture-level reliability analysis and protection	8.7 – Physical Design and Analysis	8.8 – MathWorks Tutorial	
6:00 pm									
7:00 pm									

Overview: Thursday, 12 Mar, 2020

	Amphithéâtre Jean Prouve	Chamrousse	Autrans	Stendhal	Bayard	Lesdiguières	Berlioz	Exhibition Theatre	Poster Area
8:00 am									
9:00 am	9.1 – Special Day on "Silicon Photonics": Advancements on Silicon Photonics	9.2 – Autonomous Systems Design Initiative: Architectures and Frameworks for Autonomous Systems	9.3 – Special Session: In memory computing for edge AI	9.4 – Efficient DNN design with Approximate Computing.	9.5 – Emerging memory devices	9.6 – Intelligent Dependable Systems	9.7 – Diverse Applications of Emerging Technologies	9.8 – Special Session: Panel: Variation-aware analyzes of Mega-MOSFET Memories, Challenges and Solutions	
10:00 am									IP4 Interactive Presentations
11:00 am	10.1 – Special Day on "Silicon Photonics": High-Speed Silicon Photonics Interconnects for Data Center and HPC	10.2 – Autonomous Systems Design Initiative: Uncertainty Handling in Safe Autonomous Systems (UHSAS)	10.3 – Special Session: Next Generation Arithmetic for Edge Computing	10.4 – Design Methodologies for Hardware Approximation	10.5 – Emerging Machine Learning Applications and Models	10.6 – Secure Processor Architecture	10.7 – Accelerators for Neuromorphic Computing	10.8 – Exhibition Theatre Keynote and Publisher's Session	
12:00 pm									
1:00 pm	11.0 – LUNCHTIME KEYNOTE SESSION								
2:00 pm	11.1 – Special Day on "Silicon Photonics": Advanced Applications	11.2 – Autonomous Systems Design Initiative: Autonomous Cyber-Physical Systems: Modeling and Verification	11.3 – Special Session: Emerging Neural Algorithms and Their Impact on Hardware	11.4 – Reliable in-memory computing	11.5 – Compile time and virtualization support for embedded system design	11.6 – Aging: estimation and mitigation	11.7 – System Level Security	11.8 – Special Session: Self-aware, biologically-inspired adaptive hardware systems for ultimate dependability and longevity	
3:00 pm									IP5 Interactive Presentations
4:00 pm	12.1 – Special Day on "Silicon Photonics": Design Automation for Photonics	12.2 – Autonomous Systems Design Initiative: Emerging Approaches to Autonomous Systems Design	12.3 – Reconfigurable Systems for Machine Learning	12.4 – Approximate Computing Works! Applications & Case Studies	12.5 – Cyber-Physical Systems for Manufacturing and Transportation	12.6 – Industrial Experience: From Water-Level Up to IoT Security	12.7 – Power-efficient multi-core embedded architectures	12.8 – Special Session: EDA Challenges in Monolithic 3D Integration: From Circuits to Systems	
5:00 pm									
6:00 pm									

Overview: Friday, 13 Mar, 2020

	Chamrousse	Bayard	Lesdiguières	Berlioz	Autrans 1	Villard de Lans 2	Poster Area
8:00 am							
9:00 am	W03 – SECOND DATE WORKSHOP ON AUTONOMOUS SYSTEMS DESIGN (ASD 2020)	W05 – 2ND WORKSHOP OPEN-SOURCE DESIGN AUTOMATION (OSDA 2020)	W01 – OPTICS: Optical/Photonic Interconnects for Computing Systems	W02 – COMPUTATION-IN-MEMORY (CIM): FROM DEVICE TO APPLICATIONS	W06 – STOCHASTIC COMPUTING FOR NEUROMORPHIC ARCHITECTURES (SCONA)	W07 – TRUDEVICE 2020: WORKSHOP ON TRUSTWORTHY MANUFACTURING AND UTILIZATION OF SECURE DEVICES	UB – University Booth (Autrans)
10:00 am							
11:00 am							
12:00 pm							
1:00 pm							
2:00 pm							
3:00 pm							
4:00 pm							
5:00 pm							
6:00 pm	FM01 – PhD Forum (Lunch Area)						
7:00 pm							
8:00 pm							
9:00 pm							